

L Number	Hits	Search Text	DB	Time stamp
-	275	712/32.ccls.	USPAT; US-PGPUB	2004/05/13 09:11
-	147	345/503.ccls.	USPAT; US-PGPUB	2002/08/12 15:07
-	3757	(co\$1processor (dual adj1 processor)) and address and data and (burst single)	USPAT; US-PGPUB	2002/08/12 16:31
-	1574	(co\$1processor (dual adj1 processor)) and address and data and (burst single) and memory and (control near5 register)	USPAT; US-PGPUB	2002/08/12 16:33
-	1245	(co\$1processor (dual adj1 processor)) and address and (data near5 transfer) and (burst single) and memory and (control near5 register)	USPAT; US-PGPUB	2002/08/12 16:33
-	628	(co\$1processor (dual adj1 processor)) and ((address and data) adj2 bus) and (data near5 transfer) and (burst single) and memory and (control near5 register)	USPAT; US-PGPUB	2002/08/13 08:20
-	1081	(data near5 transfer) same (burst single) and (co-processor coprocessor (dual near2 processor))	USPAT; US-PGPUB	2002/08/13 08:49
-	598	(data near5 transfer) same (burst single) and (co-processor coprocessor (dual near2 processor)) and (control near2 register)	USPAT; US-PGPUB	2002/08/13 08:50
-	567	(data near5 transfer) same (burst single) and (co-processor coprocessor (dual near2 processor)) and (control near2 register) and ((address data) near3 bus)	USPAT; US-PGPUB	2002/08/13 08:55
-	489	(data near5 transfer) same (burst single) and (co-processor coprocessor (dual adj2 processor)) and (control near2 register) and ((address data) near3 bus)	USPAT; US-PGPUB	2002/08/13 09:05
-	202	(data near5 transfer) same burst and (co-processor coprocessor (dual adj2 processor)) and (control near2 register) and ((address data) near3 bus)	USPAT; US-PGPUB	2002/08/13 10:29
-	234	712/225.ccls.	USPAT; US-PGPUB	2002/08/13 09:31
-	154	712/225.ccls. and coprocessor co-processor and burst and control adj1 register	USPAT; US-PGPUB	2002/08/13 09:35
-	196	(data near2 transfer) same burst and (co-processor coprocessor (dual adj2 processor)) and (control near2 register) and ((address data) near3 bus)	USPAT; US-PGPUB	2002/08/13 10:31
-	337	(data near2 transfer) and coprocessor co-processor (dual adj1 processor)	EPO; JPO	2002/08/13 10:33
-	250	(data near2 transfer) and coprocessor co-processor (dual adj1 processor) and burst and (address near2 bus) and (data near2 bus)	EPO; JPO	2002/08/13 10:37
-	619	dual adj processor	USPAT; US-PGPUB	2002/08/13 10:38
-	224	dual adj processor and data adj transfer	USPAT; US-PGPUB	2002/08/13 10:39
-	40	dual adj processor and data adj transfer and (co-processor coprocessor)	USPAT; US-PGPUB	2002/08/13 10:39
-	22	712/34.ccls. and burst	USPAT; US-PGPUB	2002/08/13 15:48
-	22	712/34.ccls. and burst	USPAT; US-PGPUB	2002/08/13 15:13
-	1	6321310.pn.	USPAT; US-PGPUB	2002/08/13 15:48
-	124	(coprocessor co-processor) and (internal adj1 memory) and (control adj1 register) and burst	USPAT; US-PGPUB	2002/08/13 16:15
-	28	712/32.ccls. and burst	USPAT; US-PGPUB	2002/08/15 10:09
-	6	el-kik.in.	USPAT; US-PGPUB	2002/08/14 09:45

-	13	burst adj mode adj bit	USPAT;	2002/08/14
-	16	712/35.ccls. and burst	US-PGPUB	11:32
-	28	712/32.ccls. and burst	USPAT;	2002/08/15
-	246	burst and transfer same (coprocessor co-processor)	US-PGPUB	11:10
-	509	@ad<=20000106 and (burst with transfer) and ((master and slave) co-processor coprocessor) and (register with control) and (bit with (mode burst))	USPAT;	2002/08/15
-	514	@ad<=20000106 and (burst with transfer) and ((master and slave) co-processor coprocessor) and (register with control) and ((bit flag) with (mode burst))	US-PGPUB	11:10
-	67	@ad<=20000106 and (burst with transfer) same ((master and slave) co-processor coprocessor) and (register with control) and ((bit flag) with (mode burst))	USPAT;	2002/08/15
-	186	@ad<=20000106 and 710/35.ccls.	US-PGPUB	15:00
-	12	@ad<=20000106 and 710/35.ccls. and (coprocessor co-processor)	USPAT;	2002/12/12
-	274	@ad<=20000106 and 710/33.ccls.	US-PGPUB	09:03
-	292	@ad<=20000106 and 710/110.ccls.	USPAT;	2002/12/12
-	146	@ad<=20000106 and 710/35.ccls. and burst	US-PGPUB	09:03
-	66	@ad<=20000106 and 710/33.ccls. and burst	USPAT;	2002/12/12
-	69	@ad<=20000106 and 710/110.ccls. and burst	US-PGPUB	14:49
-	30	@ad<=20000106 and burst with ((master and slave) coprocessor co-processor) and (increment with address) and (register with control) and ((mode burst) with bit)	USPAT;	2002/12/12
-	1019	DMA same (start with address)	US-PGPUB	15:29
-	806	DMA same (start with address) and ((address and data) with bus)	USPAT;	2002/12/12
-	1	5809309.pn.	US-PGPUB	15:20
-	123	(DMA with (coprocessor co-processor processor)) same (start with address) and ((address and data) with bus)	USPAT;	2002/12/12
-	10	(DMA with (controller coprocessor co-processor processor) same (internal with (memory RAM))) same (start with address) and ((address and data) with bus)	US-PGPUB	15:29
-	634	710/22.ccls.	USPAT;	2002/12/12
-	149	710/26.ccls.	US-PGPUB	15:28
-	3	(coprocessor co-processor) same (internal adj (RAM memory)) same DMA and (control adj register)	USPAT;	2004/03/24
-	8	(coprocessor co-processor) same (internal adj (RAM memory)) same DMA	US-PGPUB	15:26
-	6	("4263650"   "4292668"   "4300193"   "4378591"   "4514808"   "4530053").PN.	USPAT;	2002/12/12
-	23	4631671.URPN.	US-PGPUB	16:01
-	14	("4031517"   "4279016"   "4414624"   "4439839"   "4577282"   "4646232"   "4669043"   "4713748"   "4829475"   "4878190"   "4912636"   "4930068"   "5077657"   "5560019").PN.	USPAT;	2002/12/12
-	1	5809309.URPN.	US-PGPUB	15:29

-	333	(DMA with (controllor coprocessor co-processor processor) same (internal with (memory RAM)))	USPAT; US-PGPUB	2003/05/05 07:59
-	93	(DMA near2 (controllor coprocessor co-processor processor) same (internal with (memory RAM)))	USPAT; US-PGPUB	2003/05/05 08:00
-	66	(DMA near2 (controllor coprocessor co-processor processor) same (internal near3 (memory RAM)))	USPAT; US-PGPUB	2003/05/05 08:00
-	68	709/211.ccls.	USPAT; US-PGPUB	2004/05/12 09:39
-	9	709/211.ccls. and DMA	USPAT; US-PGPUB	2003/05/05 17:01
-	12	("4030074"   "4208650"   "4298928"   "4326291"   "4351023"   "4368514"   "4369443"   "4424565"   "4456933"   "4466098"   "4471427"   "4481578").PN.	USPAT	2003/05/05 17:06
-	1	"4099236".PN.	USPAT	2003/05/05 17:07
-	82	4700292.URPN.	USPAT	2003/05/05 17:10
-	19	4700292.URPN. and DMA	USPAT	2003/05/05 17:15
-	191	709/212.ccls.	USPAT	2003/05/06 13:21
-	236	709/216.ccls.	USPAT	2003/05/06 14:17
-	25	transputer and INMOS.as.	USPAT	2003/05/06 07:51
-	14	transputer and INMOS.as. and transfer	USPAT	2003/05/06 07:52
-	0	transputer and INMOS.as. and DMA	USPAT	2003/05/06 07:52
-	23	transputer and INMOS.as. and array	USPAT	2003/05/06 07:56
-	104	INMOS.as.	USPAT	2003/05/06 07:57
-	5	INMOS.as. and 712/\$3.ccls.	USPAT	2003/05/06 07:59
-	907	712/10-22.ccls.	USPAT	2003/05/06 08:02
-	136	712/10-22.ccls. and DMA	USPAT	2003/05/06 08:04
-	630	712/10-22.ccls. and transfer	USPAT	2003/05/06 08:05
-	56	712/10-22.ccls. and transfer with DMA	USPAT	2003/05/06 14:45
-	192	709/216.ccls. and (transfer\$3 DMA)	USPAT	2003/05/06 14:17
-	142	710/34.ccls.	USPAT	2003/05/06 14:45
-	1	5193204.pn.	USPAT; US-PGPUB	2004/01/05 09:18
-	248	710/35.ccls.	USPAT; US-PGPUB	2004/03/24 15:47
-	106	710/35.ccls. not (counter count)	USPAT; US-PGPUB	2004/05/11 14:19
-	302	g06f\$.ipc. and (burst near3 (mode transfer\$4)) same address near5 (apply\$3 applied maintain\$3 latch\$3 assert\$3)	USPAT; US-PGPUB	2004/05/11 14:23
-	250	g06f\$.ipc. and (burst near3 (mode transfer\$4)) same address near5 (apply\$3 applied maintain\$3 latch\$3 assert\$3)	USPAT	2004/05/11 15:43
-	2	g06f\$.ipc. and (burst near3 (mode transfer\$4)) same (coprocessor co-processor slave) near3 address near5 (apply\$3 applied maintain\$3 latch\$3 assert\$3)	USPAT	2004/05/11 15:00

-	29	g06f\$.ipc. and (burst near3 (mode transfer\$4)) same address near5 (apply\$3 applied maintain\$3 latch\$3 assert\$3) and ((plurality multiple) with (slave coprocessor co-processor))	USPAT	2004/05/11 15:53
-	101	g06f\$.ipc. and (master main host) near2 (processor CPU) same ((plurality multiple) with (slave coprocessor co-processor)) and ((slave coprocessor co-processor) near3 address)	USPAT	2004/05/11 15:54
-	45	g06f\$.ipc. and (master main host) near2 (processor CPU) same ((plurality multiple) with (slave coprocessor co-processor)) and ((slave coprocessor co-processor) near3 address) with (communicat\$3 transfer\$4)	USPAT	2004/05/11 15:57
-	25	g06f\$.ipc. and (master main host) near2 (processor CPU) same ((plurality multiple) with (slave coprocessor co-processor)) and ((slave coprocessor co-processor) with instruction with (address number)) with (communicat\$3 transfer\$4)	USPAT	2004/05/11 16:00
-	110	g06f\$.ipc. and select\$3 near3 (coprocessor slave) with transfer\$4	USPAT	2004/05/11 16:01
-	60	g06f\$.ipc. and (activ\$5 select\$3) near3 (coprocessor slave) with transfer\$4 with address	USPAT	2004/05/11 16:16
-	0	g06f\$.ipc. and coprocessor with transfer\$4 with match\$3 near5 address	USPAT	2004/05/11 16:17
-	0	g06f\$.ipc. and coprocessor with match\$3 with address same transfer\$4	USPAT	2004/05/11 16:17
-	1	g06f\$.ipc. and (coprocessor co-processor) with match\$3 with (number ID address) same transfer\$4	USPAT	2004/05/11 16:18
-	70	712/31.ccls.	USPAT; US-PGPUB	2004/05/11 16:21
-	218	712/34.ccls.	USPAT; US-PGPUB	2004/05/11 16:21
-	73	(712/31.ccls. 712/34.ccls.) and (multiple plurality two) near2 (slave coprocessor co-processor)	USPAT; US-PGPUB	2004/05/11 16:22
-	1	709/211.ccls. and burst	USPAT; US-PGPUB	2004/05/12 09:37
-	479	(burst block) near2 (transfer\$4 mode) same (coprocessor co-processor slave) with (master ((main host) near2 CPU processor))	USPAT	2004/05/12 09:41
-	479	(burst block) near2 (transfer\$4 mode) same (coprocessor co-processor slave) with (master ((main host) near2 CPU processor))	USPAT	2004/05/12 09:45
-	346	(burst block) near2 (transfer\$4 mode) same (coprocessor co-processor slave) with (master ((main host) near2 CPU processor)) and g06f\$.ipc.	USPAT	2004/05/12 09:48
-	143	(burst block) near2 (transfer\$4 mode) same (coprocessor co-processor slave) with (master ((main host) near2 CPU processor)) and g06f\$.ipc. and ((apply\$3 applied maintain\$3 assert\$3 latch\$3) with address same (burst block))	USPAT	2004/05/12 09:50
-	205	(burst block) near2 (transfer\$4 mode) same (coprocessor co-processor slave) with (master ((main host) near2 CPU processor)) and g06f\$.ipc. and ((apply\$3 applied maintain\$3 assert\$3 latch\$3 writ\$3) with address same (burst block))	USPAT	2004/05/12 09:53

-	156	(burst block) near2 (transfer\$4 mode) same (coprocessor co-processor slave) with (master ((main host) near2 CPU processor)) and g06f\$.ipc. and ((apply\$3 applied maintain\$3 assert\$3 latch\$3 writ\$3) with address same (burst block)) not ((count number) near3 words)	USPAT	2004/05/12 14:02
-	2	asynchronous with burst with (transfer\$4 mode) same (coprocessor slave)	USPAT	2004/05/12 14:03
-	6	burst with (transfer\$4 mode) same (coprocessor slave)	EPO	2004/05/12 14:05
-	17	burst with (transfer\$4 mode) same (coprocessor slave)	JPO	2004/05/12 14:09
-	39	burst with (transfer\$4 mode) same (coprocessor slave)	DERWENT; IBM TDB	2004/05/12 16:43
-	9	("5109329"   "5361362"   "5412803"   "5655082"   "5951683"   "5987506"   "6018782"   "6112230"   "6144995").PN.	USPAT	2004/05/12 14:23
-	0	coprocessor same memory near3 (partition\$3 divid\$3) with (block bank)	DERWENT; IBM TDB	2004/05/12 16:44
-	11	coprocessor same memory near3 (partition\$3 divid\$3) with (block bank)	USPAT;	2004/05/12 16:55
-	5743	memory near3 (plurality multiple) near2 (block partition)	US-PGPUB USPAT;	2004/05/12 16:55
-	49	memory near3 (plurality multiple) near2 (block partition) with (benefit advantage)	USPAT; US-PGPUB	2004/05/12 16:56
-	1	5901103.pn.	USPAT; US-PGPUB	2004/05/13 09:11